



晶采光電科技股份有限公司AMPIRE CO., LTD.

Specifications for LCD module

Customer	
Customer part no.	
Ampire part no.	AM-320240NTMQW-30H-D(R)
Approved by	
Date	

- □ Preliminary Specification
- Formal Specification

AMPIRE CO., LTD.

4F., No.116, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City221, Taiwan (R.O.C.)

新北市汐止區新台五路一段 116 號 4 樓(東方科學園區 A 棟)

TEL:886-2-26967269 , FAX:886-2-26967196 or 26967270

Approved by	Checked by	Organized by
Patrick	Kokai	Jessica

This Specification is subject to change without notice.

Date: 2021/12/24 AMPIRE CO., LTD. 1

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2021/09/28		New Release	Jessica
2021/11/15	21	Remove screen saver description	Jessica
2021/12/24	3,22,23	Update drawings	Jessica
		invshop.c	

2

1 Features

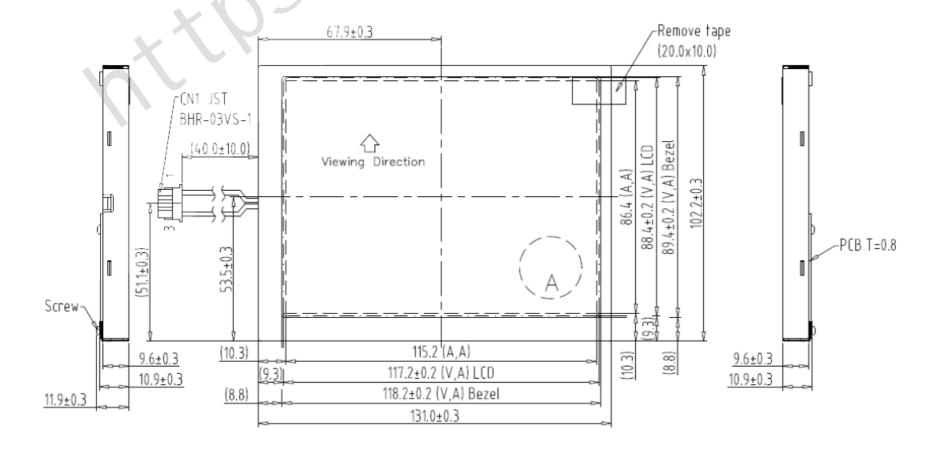
5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 5.7" TFT-LCD panel, a driver circuit and backlight unit.

- (1) Construction: 5.7" a-Si color TFT-LCD, White LED Backlight and PCB.
- (2) Resolution (pixel): 320(R.G.B) X240
- (3) Number of the Colors: 262K colors (R, G, B 6 bit digital each)
- (4) LCD type: Transmissive Color TFT LCD (normally White)
- (5) Interface: 40 pin. DE only mode support.
- (6) Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.
- (7) Viewing Direction: 6 O'clock (gray inversion)
- (8) New PCBA

Date: 2021/12/24

2 Physical Specifications

Item	Specifications	Unit
Display resolution(dot)	960 (W) x 240(H)	mm
Screen size	5.7(Diagonal)	mm
Pixel size	120 (W) x 360 (H)	um
Color configuration	R.G.B stripe	
Weight	T.B.D	mg
Backlight unit	LED	



3 Electrical specification

3.1 Absolute max. ratings

3.1.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VDD	VSS=0	-0.3	6.0	V	
Input voltage	Vin		-0.3	VDD+0.3	V	Note(1)

Note(1) Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

3.1.2 Environmental Absolute max. ratings

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Item	MIN	MAX	MIN	MAX	Remark
Temperature	-20	70	-30	80	Note(2)(3)(4)(5)(6)(7)
Humidity	Note(1)		Not	e(1)	10.
Corrosive Gas	Not Acc	eptable	Not Acc	eptable	

Note(1) Ta <= 40°C: 85% RH max

Ta > 40°C: Absolute humidity must be lower than the humidity of 85%RH at 40°C

Note(2) For storage condition Ta at -30°C < 48h , at 80° C < 100h

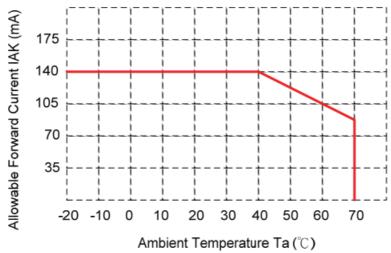
For operating condition Ta at -20°C < 100h

Note(3) Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note(4) The response time will be slower at low temperature.

Note(5) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at +25°C

Note(6) When LCM is operated over 40°C ambient temperature, the IAK of the LED back-light should be follow:



Note(7) This is panel surface temperature, not ambient temperature.

Note(8) When LCM is operated over than 40°C, the life time of the LED back-light will be reduced.

3.2 Electrical characteristics

Date: 2021/12/24

3.2.1 DC Electrical characteristic of the LCD

Typical operating conditions (VSS=0V)

Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
Power supply		VDD	3.0	3.3	3.6	٧	
Input Voltage	H Level	ViH	0.7 VDD	-	VDD	V	Noto(1)
for logic	L Level	V _{IL}	0	-	0.3 VDD	V	Note(1)
Power Supply current		IDD		45	55	mA	Note(2)

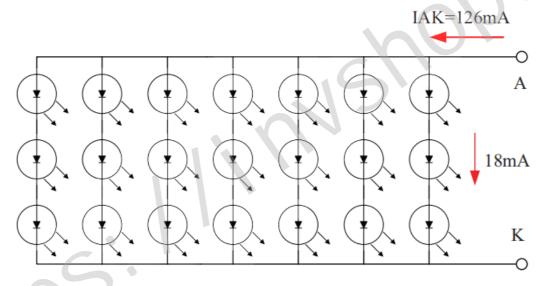
Note(1) Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

Note(2) fv =60Hz , Ta=25°C , Display pattern : All Black

3.2.2 Electrical characteristic of LED Back-light

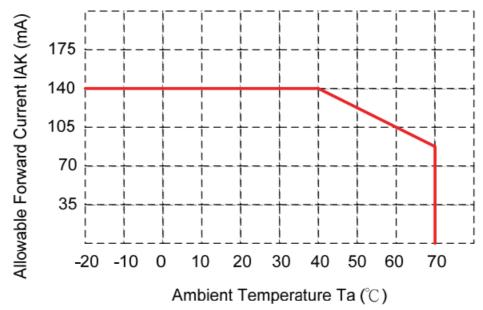
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
LED voltage	VAK	8.4	9.6	10.8	V	IAK=140mA Ta=25°C
LED forward current	IAK		126	140	mA	Ta=25°C
LED forward current	IAK		84	105	mA	Ta=60°C
LED life time			30K		hours	Note(1)

- Note(1) End of Life shall be determined by the time when any of the following is satisfied under continuous lighting at 25°C and IAK = 126mA.
 - Intensity drops to 50% of the Initial Value (Min. Luminance)
 - Based on LED
 - It is an estimative value



- Note(2) The constant current source is needed for white LED back-light driving.
- Note(3) When LCM is operated over 60°C ambient temperature, the IAK of the LED

back-light should be adjusted to 105mA max



3.3 AC Timing characteristic of the LCD

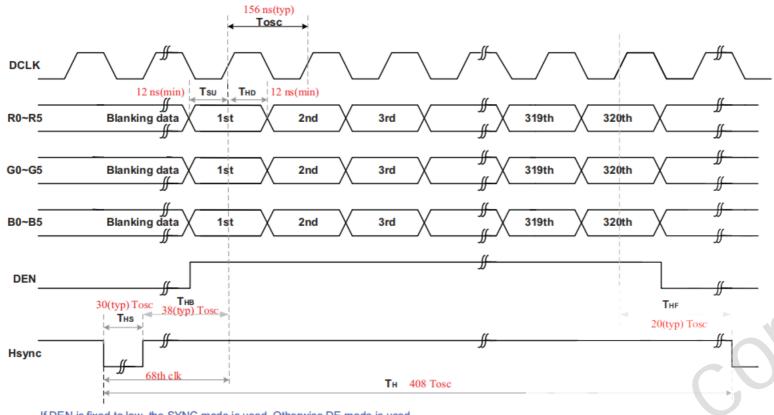
3.3.1 Timing condition

Signal	Parameter	Symbol	Min.	Тур.	Max.	Unit.	Remark	
	DCLK period		TOSC	ı	156	ı	ns	
DCLK	Frequency		FOSC	ı	6.4	ı	MHz	
	DCLK High plus	width	TCH	ı	78	ı	ns	
	DCLK Low plus v	vidth	TCL	•	78	ı	ns	
RGB	Data setup tim	ne	TSU	12	-	-	ns	
DATA	Data hold tim	е	THD	12	-	-	ns	
	Hsync period	l	TH	-	408	-	TOSC	
	Hsync pulse wi	dth	THS	5	30	-	TOSC	
Цоуго	Back-Parch		THB		38		TOSC	3
Hsync	Front-Parch		THF		20		TOSC	
	Hsync rising tir	ne	TCr	-	-	700	ns	
	Hsync falling tir	TCf	-	-	300	ns		
	Voune period	NTSC		-	262.5	- 0	TH	
	Vsync period	PAL			312.5	Y -	TH	
	Vsync pulse wi	dth	TVS	1	3	5	TH	
	Dook Dorok	NTSC	TVB	S	15		TH	
	Back-Porch	PAL			23		TH	
	Display Perio	d	TVD		240		TH	
Vsync	Front Doroh	NTSC	TVE		4.5		TH	
	Front Porch	PAL	TVF		46.5		TH	
	Vsync rising tir	ne	TVr	-	-	700	ns	
	Vsync falling tir	me	TVf	-	-	1.5	μs	
	Vsync falling to Hsyr time for odd fie	_	THVO	1	ı	1	TOSC	
	Vsync falling to Hsync falling time for even fiel		THVE	1	ı	1	TOSC	
	Voyna DEN time	NTSC	TVSE	ı	18	ı	TH	
DEN	Vsync-DEN time PAL		TVSE	-	26	-	TH	
DEN	Hsync-DEN tin	ne	THE	36	68	88	TOSC	
	DEN plus widt	th	TEP	-	320	-	TOSC	

- Note(1) DEN is definition of above timing for Hsync and Vsync.
- Note(2) No matter when Hsync and Vsync is inputted, the LCM can be drove only DEN Signal. DEN should be set to low level when it is not input.
- Note(3) The Dummy Enable is needed in the end of frame. Please set the LCD controller timing to 241 lines.

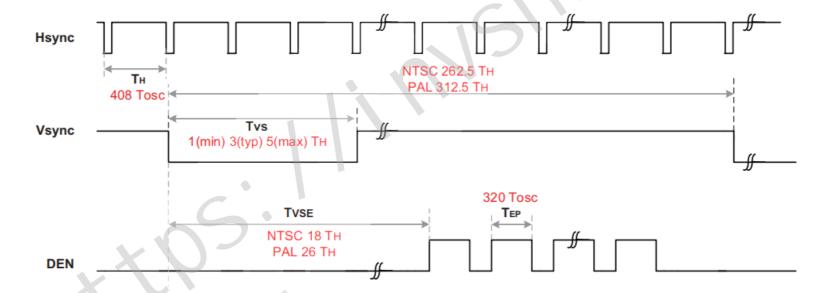
Horizontal display timing

Note: Data is latched rising edge trigger of CLK.

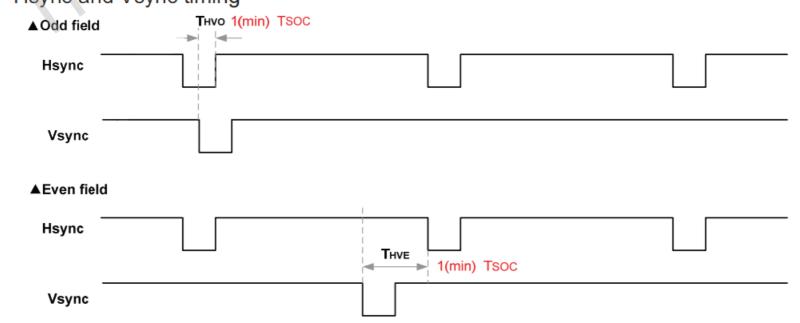


If DEN is fixed to low, the SYNC mode is used. Otherwise DE mode is used. When SYNC mode is used, 1st data start from 68th CLK after Hsync falling

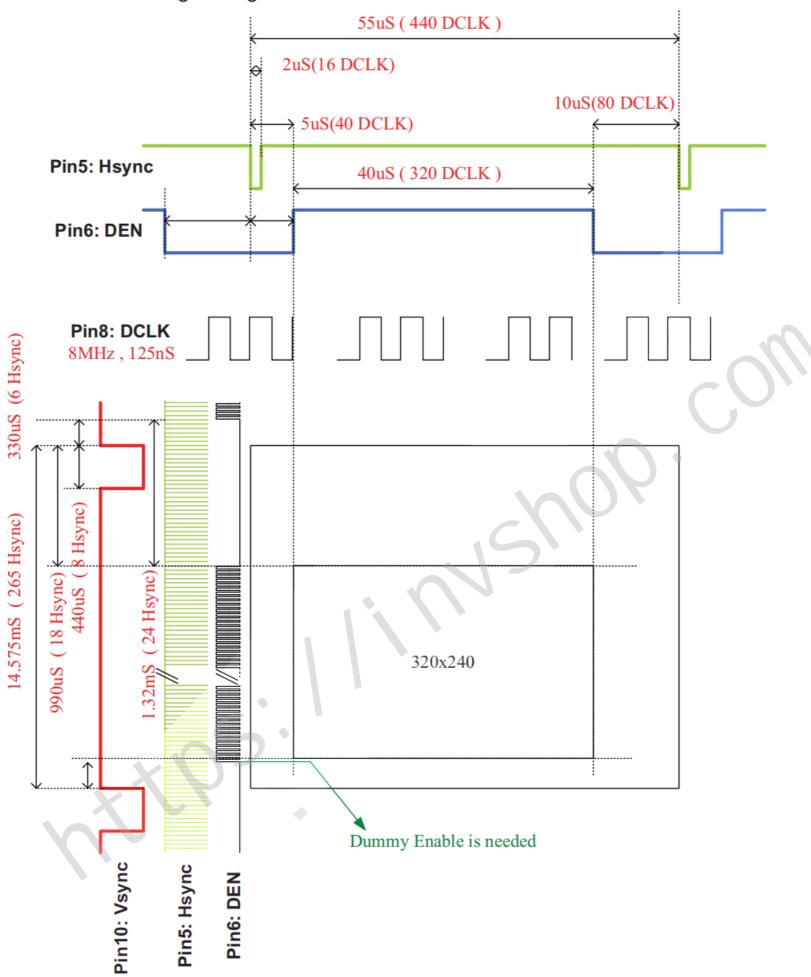
Vertical display timing



Hsync and Vsync timing



Recommend Driving Timing:



4 Optical specification

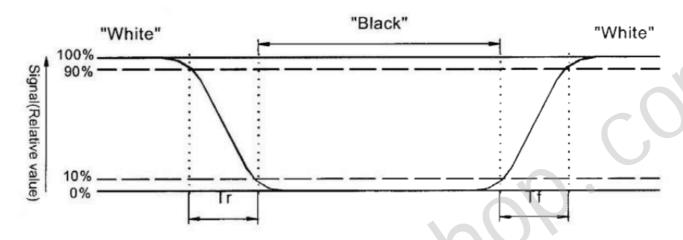
4.1 Optical characteristic of the LCD

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Response Time	Rise Fall	T _r T _f	Θ=0°	1 1	15 35	30 50	ms ms	Note 1,2,3,5
Contrast	ratio	CR	At optimized viewing angle	200	350	ı		Note 1,2,4,5
Viewing Angle	Top Bottom Left Right		CR≧10	60 40 60 60	70 50 70 70	1 1 1 1	deg.	Note1,2, 5,6
Deialeta	Brightness V.		IAK=126mA, 25°C	427.5	450	-	cd/m²	Nata 7
Brightne			IAK=140mA, 25°C	475	500	-	cd/m²	Note 7
Pod obrom	acticity	XR			0.640	<i>Y</i> .		
Red chrom	ialicity	YR		10	0.344			Note 7
Croon obro	maticit.	XG		N -	0.298			For reference
Green chroi	maticity	YG	Θ=0°	Тур.	0.583	Тур.		only. These data
Blue chron	acticity	XB	Θ=0°	-0.05	0.132	+0.05		should be update
Blue chromaticity		YB			0.137			according the
White observ	NAME OF THE PARTY				0.312			prototype.
White chror	naticity	YW			0.329			

It's for reference only. These data should be update according the prototype.

- Note(1) Ambient temperature=25°C, and lamp current IAK=140mA. To be measured in the dark room.
- Note(2) To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7, after 10 minutes operation.
- Note(3) Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note(4) Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Note(5) White
$$V_i = V_{i50} + 1.5V$$

Black $V_i = V_{i50} + 2.0V$

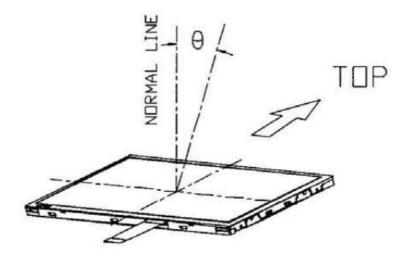
Date: 2021/12/24

"±". It means that the analog input signal swings in phase with V_{COM} signal.

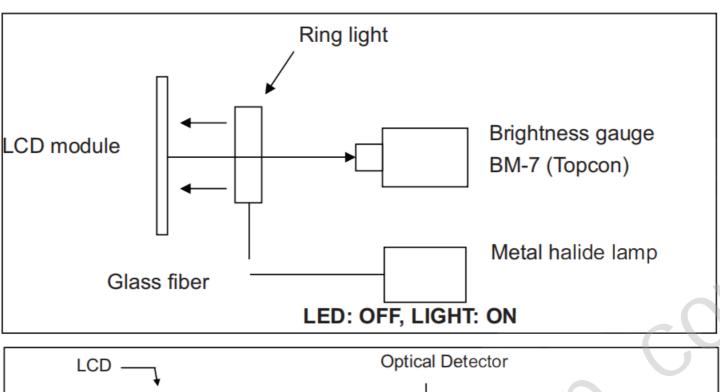
"∓". It means that the analog input signal swings out of phase with V_{COM} signal.

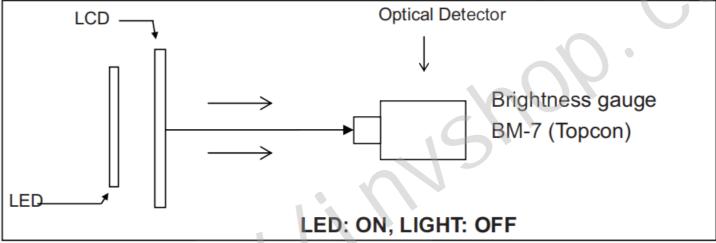
 V_{i50} : The analog input voltage when transmission is 50%. The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note(6) Definition of viewing angle, Refer to figure as below.



Note(7) Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.



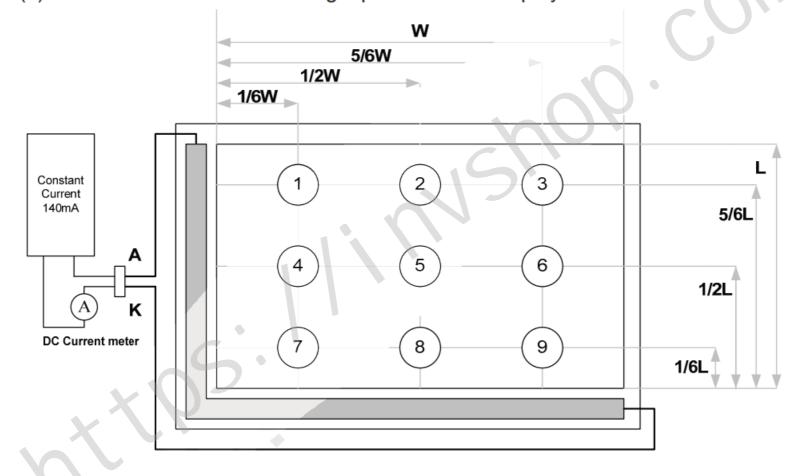


4.2 Optical characteristic of the LED Back-light

Item	Min.	Тур.	Max.	Unit	Condition
Bare Brightness	3500	1		Cd/m2	IAK =140mA,Ta=25°C
AVG. X of 1931 C.I.E.	0.28	0.31	0.34		IAK =140mA,Ta=25°C
AVG. Y of 1931 C.I.E.	0.28	0.31	0.34		IAK =140mA,Ta=25°C
Brightness Uniformity	80			%	IAK =140mA,Ta=25°C

It's for reference only. These data should be update according the prototype.

- Note(1) Measurement after 10 minutes from LED operating.
- Note(2) Measurement of the following 9 places on the display.



Note(3) The Uniformity definition (Min Brightness / Max Brightness) x 100%

5 Interface specifications

5.1 Driving signals for the TFT panel

JAE: FA5B040HF1R3000 (Suitable FPC: t=0.3+/-0.03mm, 0.5+/-0.03mm pitch)

Pin no	Symbol	I/O	Description	Remark
1~4	VDD		Power supply for the logic (3.3V)	
5	NC	ı	No connection Can be OPEN fixed to VDD or GND.	
6	DEN		Input data enable control	
7	VSS		GND	
8	DCLK	1	Clock signal. Latching data at the rising edge.	
9	VSS		GND	-0
10	NC	ı	No connection Can be OPEN fixed to VDD or GND.	
11	VSS		GND	
12	B5	Ι		
13	B4	Ι	Blue data	
14	В3	1		
15	VSS		GND	
16	B2	1		
17	B1	1	Blue data	
18	В0	1		
19	VSS		GND	
20	G5	I		
21	G4		Green data	
22	G3			
23	VSS			
24	G2	Ι		
25	G1	-1	Green data	
26	G0	1		
27	VSS		GND	
28	R5	1		
29	R4	-1	Red data	
30	R3	Ι		
31	VSS		GND	
32	R2	Ι		
33	R1	Ι	Red data	
34	R0	1		

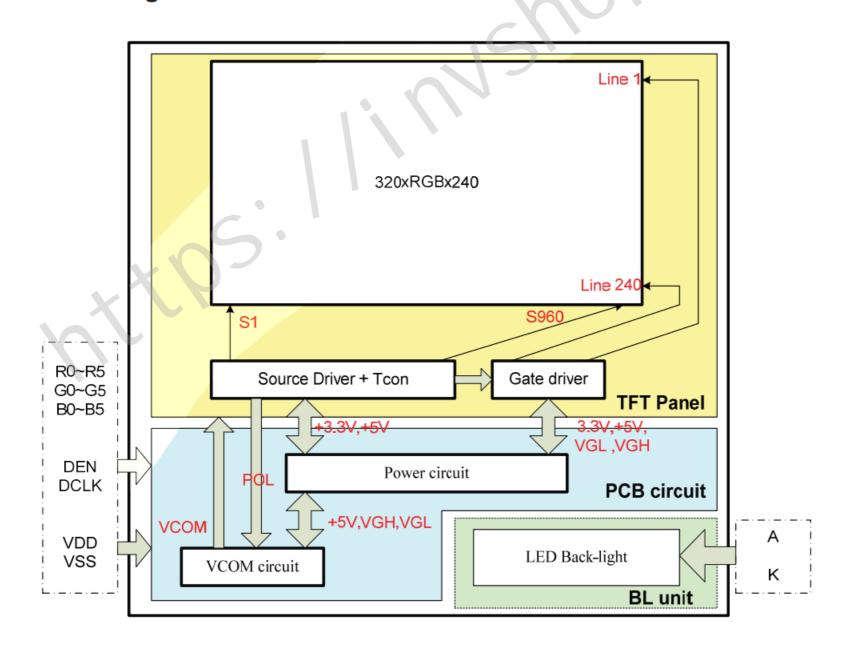
35	NC	No connection	
36	VSS	GND	
37	XT	Touch Panel Top Signal	
38	YL	Touch Panel Left Signal	
39	XB	Touch Panel Bottom Signal	
40	YR	Touch Panel Right Signal	

5.2 Driving signals for the LED backlight

JST Housing: BHR-03VS-1

Pin no	Symbol	Level	Description	Remark
1	Α	-	LED Anode	2
2	NC	-	No connection	
3	K	-	LED Cathode	

6 Block Diagram



7 Displayed Color and Input Data

	Color & Gray									ATA S	SIGNA	L							
	Scale	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	::	:	:	:	:	:	:	:	:	:	:	:
Reu	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:		:		••		:	:		:	:	:	:	:	:
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1 .	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Blue	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
		:	:	:	:		:		:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:		:	V :	:	:	:	:	:	:	:	:	:
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

8 Quality and Reliability

8.1 Test Conditions

(1) Tests should be conducted under the following conditions:

Ambient temperature: $25 \pm 5^{\circ}$ C

Humidity : $60 \pm 25\%$ RH.

8.2 Sampling Plan

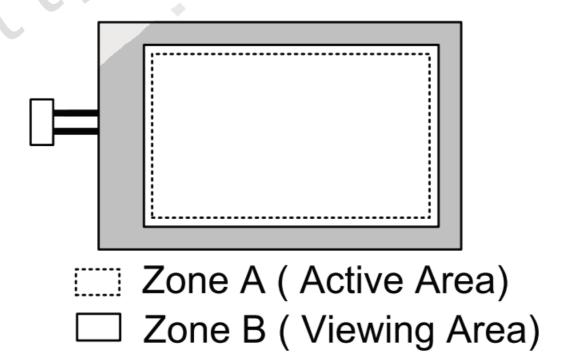
(1) Sampling method shall be in accordance with MIL-STD-105E, level II, normal single sampling plan.

8.3 Acceptable Table Quality Level

(1) A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

8.4 Appearance

(1) An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under flourescent light. The inspection area of LCD panel shall be within the range of following limits.



8.5 Reliability

Test Item	Test Conditions							
High Temperature Operation	70±3°C , t=96 hrs							
Low Temperature Operation	-20±3°C , t=96 hrs							
High Temperature Storage	80±3°C , t=96 hrs							
Low Temperature Storage	-30±3°C , t=96 hrs							
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 m in. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2						
Humidity Test	40 °C, Humidity 90%, 96 hrs							
Vibration Test (Packing)	Sweep frequency: 10 ~ 55 ~ 10 Hz/1min Amplitude: 0.75mm Test direction: X.Y.Z/3 axis Duration: 30min/each axis							

- Note(1) Condensation of water is not permitted on the module.
- Note(2) The module should be inspired after 1 hour storage in normal conditions (15~35 $^{\circ}$ C, 45~65%RH).
- Note(3) The module shouldn't be tested over one condition, and all the tests are independent.
- Note(4) All reliability tests should be done without the protective film.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of initial value.

9 Use Precautions

9.1 Handling precautions

- (1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- (2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- (3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- (4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

9.2 Installing precautions

- (1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. $1M\Omega$ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- (2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- (3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- (4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

9.3 Storage precautions

- (1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- (2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- (3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

9.4 Operating precautions

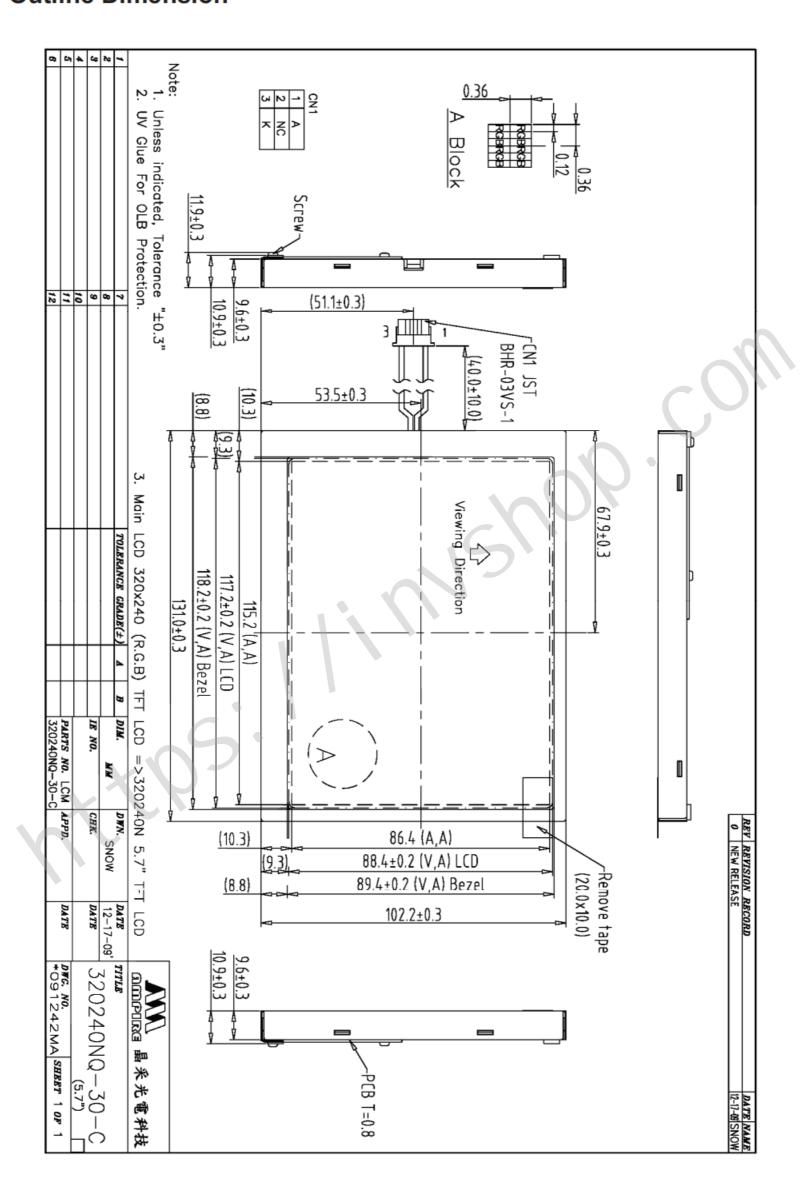
- (1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- (2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- (3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
- (4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- (5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- (6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- (7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- (8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

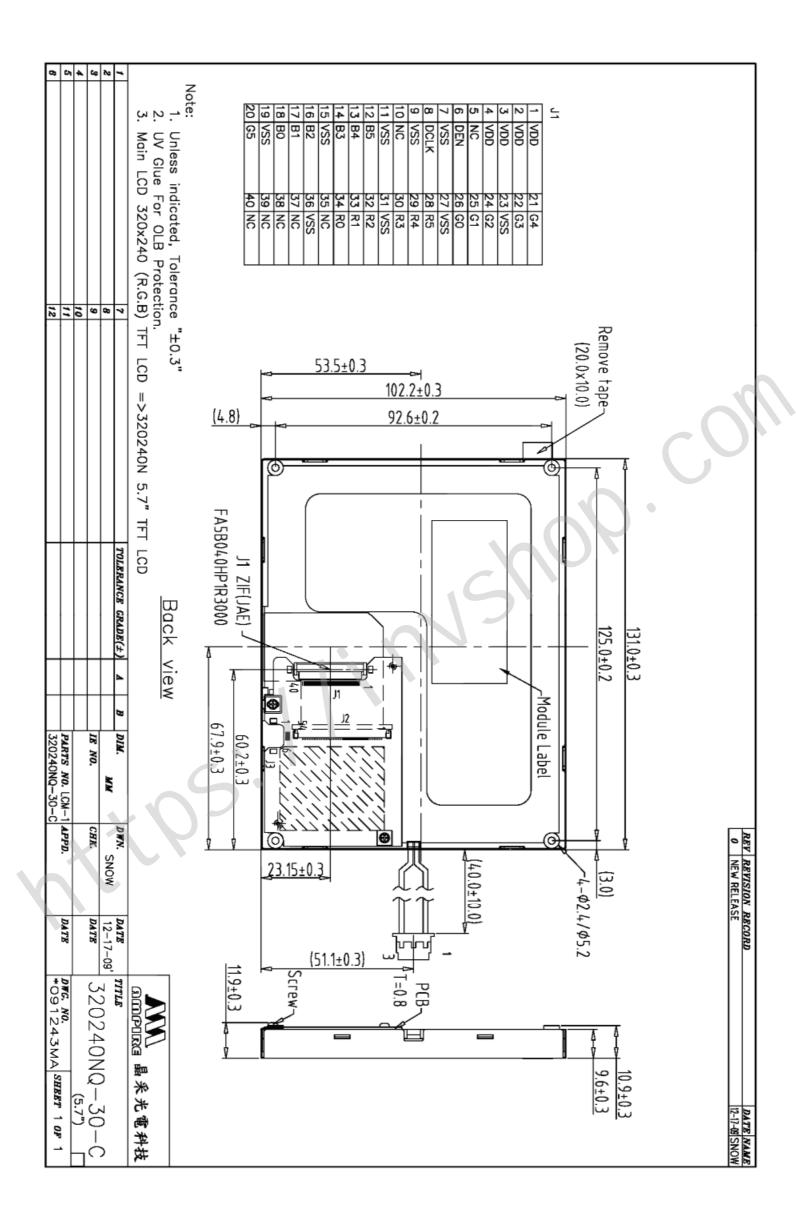
9.5 Other

- (1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- (2) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.



10 Outline Dimension





11 Package

